

**EE/CpE 423-424  
WEEKLY STATUS REPORT**

<b>Group #</b>	<b>09</b>	<b>Week ending:</b>	<b>02/29/04</b>	<b>Report #</b>	<b>6</b>
<b>Project Title:</b>	<b>GPS Mass Transit Tracker</b>				
<b>Group Leader:</b>	<b>A. Zimmer</b>	<b>Advisor:</b>	<b>Prof. McNair</b>		
<b>Sponsor/Client:</b>					
<b>Total number of person-hours spent on project by group during past week:</b>				<b>22</b>	
<b>Is project on schedule?</b>		<b>Yes</b>	<input checked="" type="checkbox"/> <b>[ x ]</b>	<b>No</b>	<input type="checkbox"/> <b>[ ]</b>

<b>Weekly status:</b>
<p>This week a lot of time was spent on constructing the perfboard and testing the micro and LCD. Tom's report is as follows:</p> <p>All parts ordered to interface the microcontroller board, LCD display, and RF module have been received. In addition to the main components, this includes a perfboard, headers/receptacles, and assembly hardware. The interconnections for the micro and LCD were soldered and wired on the perfboard. The micro board was mated to the perfboard over its headers and standoffs; and the LCD module and perfboard were mounted over standoffs on a sheet of aluminum. The RF module still needs to be mounted on the perfboard.</p> <p>Even though the micro board and interface board for the RF module have onboard regulators, the power interface to all 3 components is consolidated using a 5VDC regulated supply with an output of 3amps. The assembly with the micro board and LCD was powered to verify connections and operation. The following tests were performed: power-up, communication with a PC (Hyperterminal), and microcontroller functionality (demo program loaded in Flash). Software is being written to test the C compiler (ImageCraft ICC12) and LCD functionality. This will be followed by integration of the RF module.</p> <p>Andrew has also continued to work on an application for sending GPS data to the transceiver from a PC. Communications were established and a small test program was written to confirm handshake and data communications.</p>

**Weekly report is due to Senior Design Coordinator by Noon Monday**