

EE/CpE 423-424  
WEEKLY STATUS REPORT

<b>Group #</b>	<b>12</b>	<b>Week ending: (Saturday)</b>	<b>10/22/05</b>	<b>Report #</b>	<b>06</b>
<b>Project Title:</b>	<b>Data Acquisition and Storage System</b>				
<b>Group Leader:</b>	<b>Joseph Kilroy</b>	<b>Advisor:</b>	<b>Bruce McNair</b>		
<b>Sponsor/Client:</b>	<b>Davidson Laboratories</b>				
<b>Total number of person-hours spent on project by group during past week:</b>				<b>30</b>	
<b>Is project on schedule?</b>		<b>Yes</b>	<b>[ X ]</b>	<b>No</b>	<b>[ ]</b>

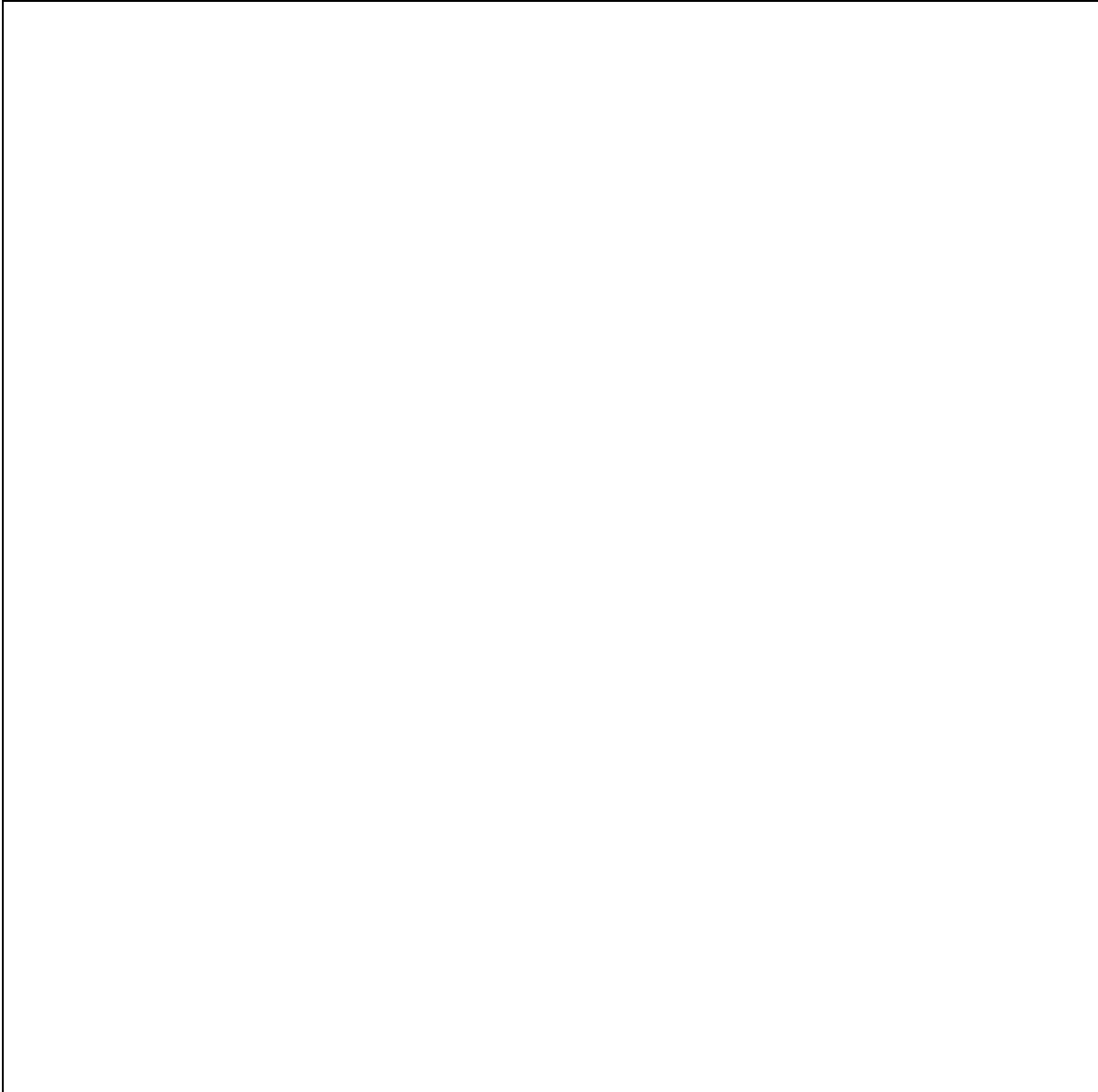
**Weekly status:**

The group meeting on Tuesday with Professor McNair proved to be a fairly quiet one, mostly with the group asking questions regarding whether it is on time or not and professor McNair advising that preparations begin for the final presentation.

When the group met on Thursday, the primary aim was to get back on track. Jeremiah and Shaina will be researching final hardware configurations, emailing Prof. Chandramouli to find out about the MicroITX, looking into what kind of flash memory will be used, and researching USB A to B adapter issues. Joe and Pauline are going to be laying out the pseudo code for the project. Pauline will be writing pseudo code for user data extra and Joe will be writing pseudo code for the serial controller. It is hoped that if at least two pieces of pseudo code are laid out each week for the rest of the semester, then the program will be easier to construct next semester.

**Weekly report is due to Senior Design Coordinator and TA by Noon Monday. Email submission is greatly preferred - submit using WebCT, email to sd@koala.ece.stevens-tech.edu or individual emails. Copy should be sent to advisor**

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