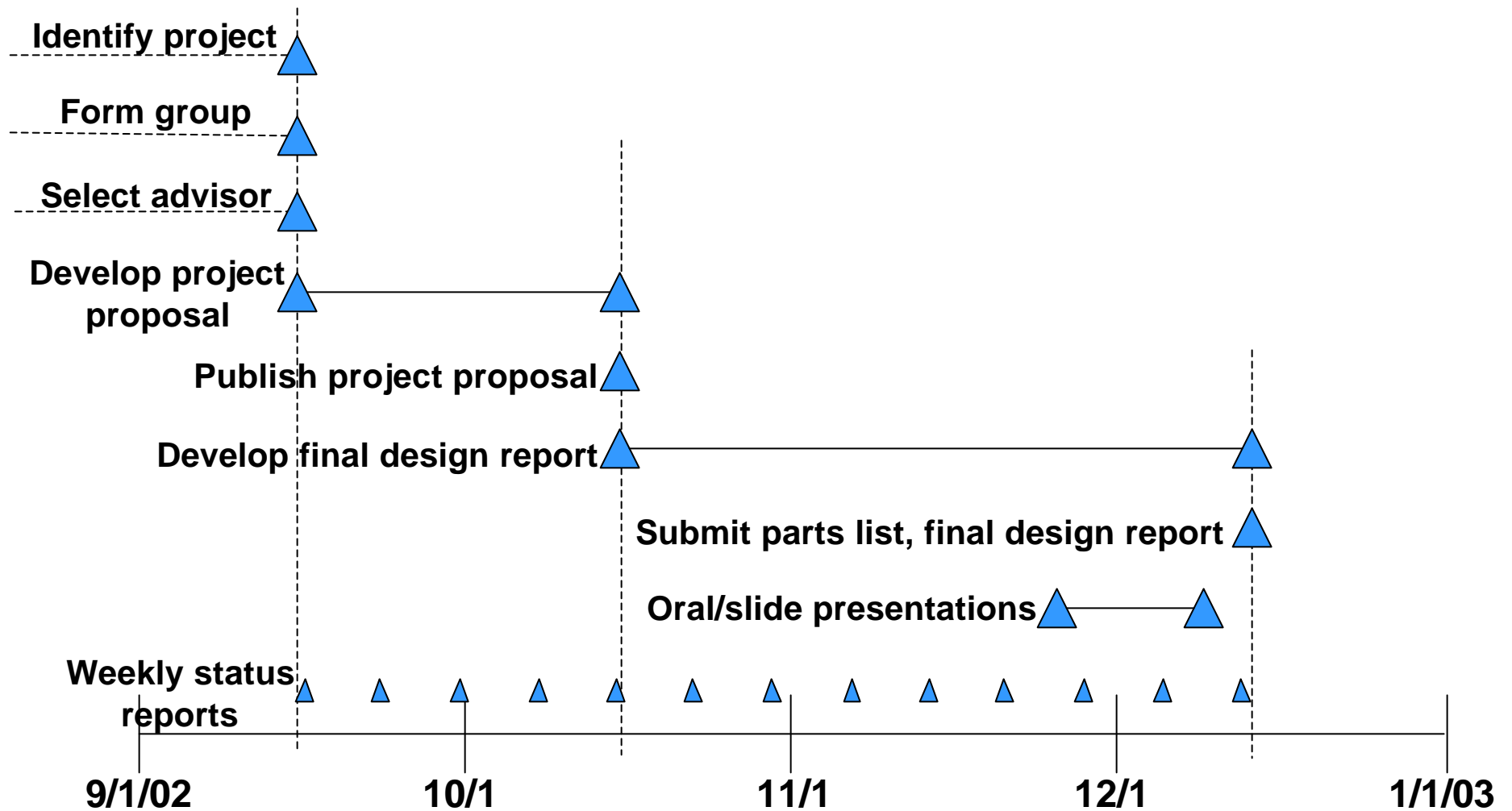
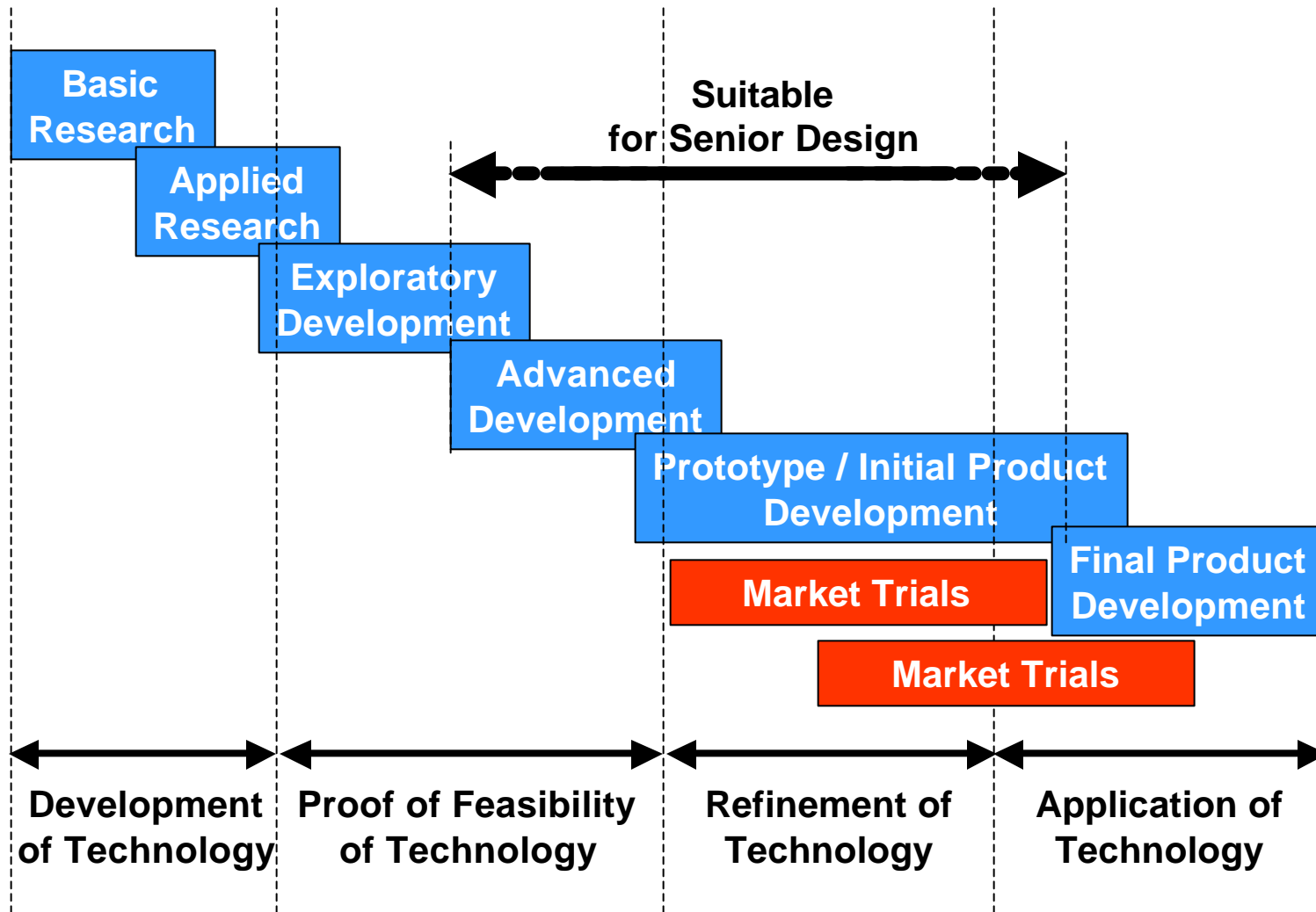


Senior Design Tasks – Fall '02



Stages in Development Cycle



Constraints That Can (Should?) Be Relaxed For Prototype

- **Physical size**
 - Level of integration (VLSI vs. FPGA, mC, LSI/MSI, etc.)
 - **Implementation platform**
 - simulating a Palm on a laptop,
 - simulating functions in software that would normally be in hardware, and vice versa
 - **Feature set**
 - What is essential to demonstrate concept, vs. what could be imagined/assumed
 - What is known to be doable vs. what is to be demonstrated
 - **Performance**
 - Speed, capacity, etc.
 - **Environmental constraints**
 - Operating temperature range, shock, vibration, etc.
- **Focus on key attributes of end design, not every detail**