

EE/CpE 423

Senior Design

Fall 2003

Class 2 – 9/9/03

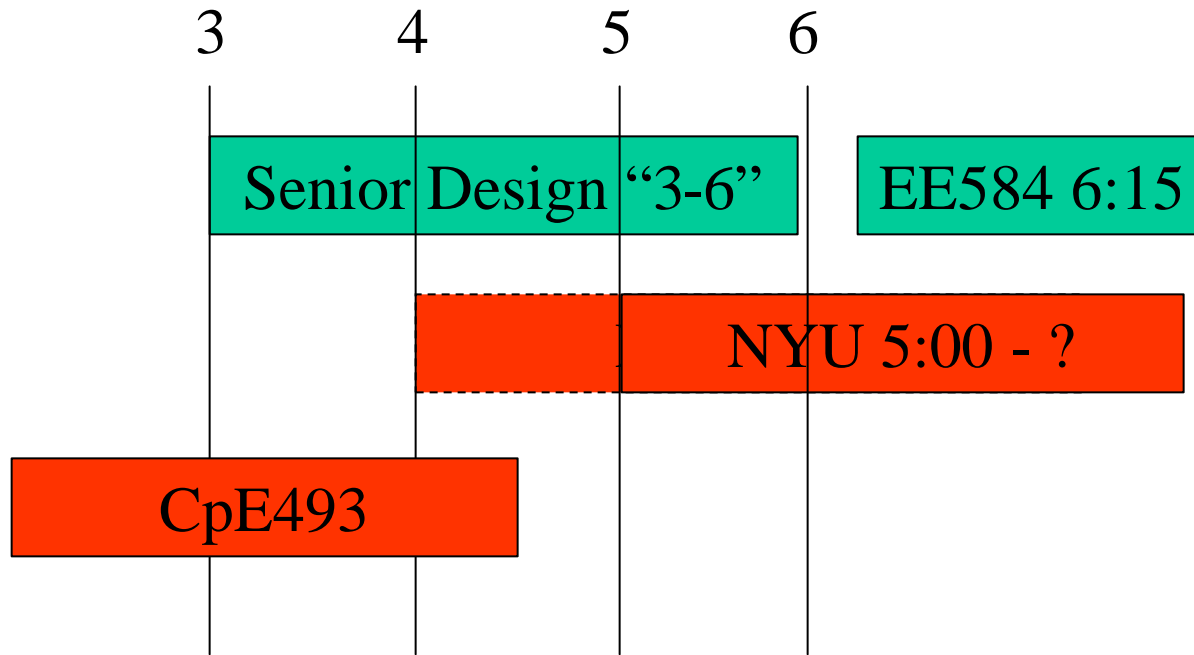
Agenda

- Donna Beyer – Career Services
– <http://attila.stevens-tech.edu/ocs>
- Dominik Hepp – A view from the '02-'03 trenches
- Announcement, issues
- Bruce McNair – Directions and more logistics.

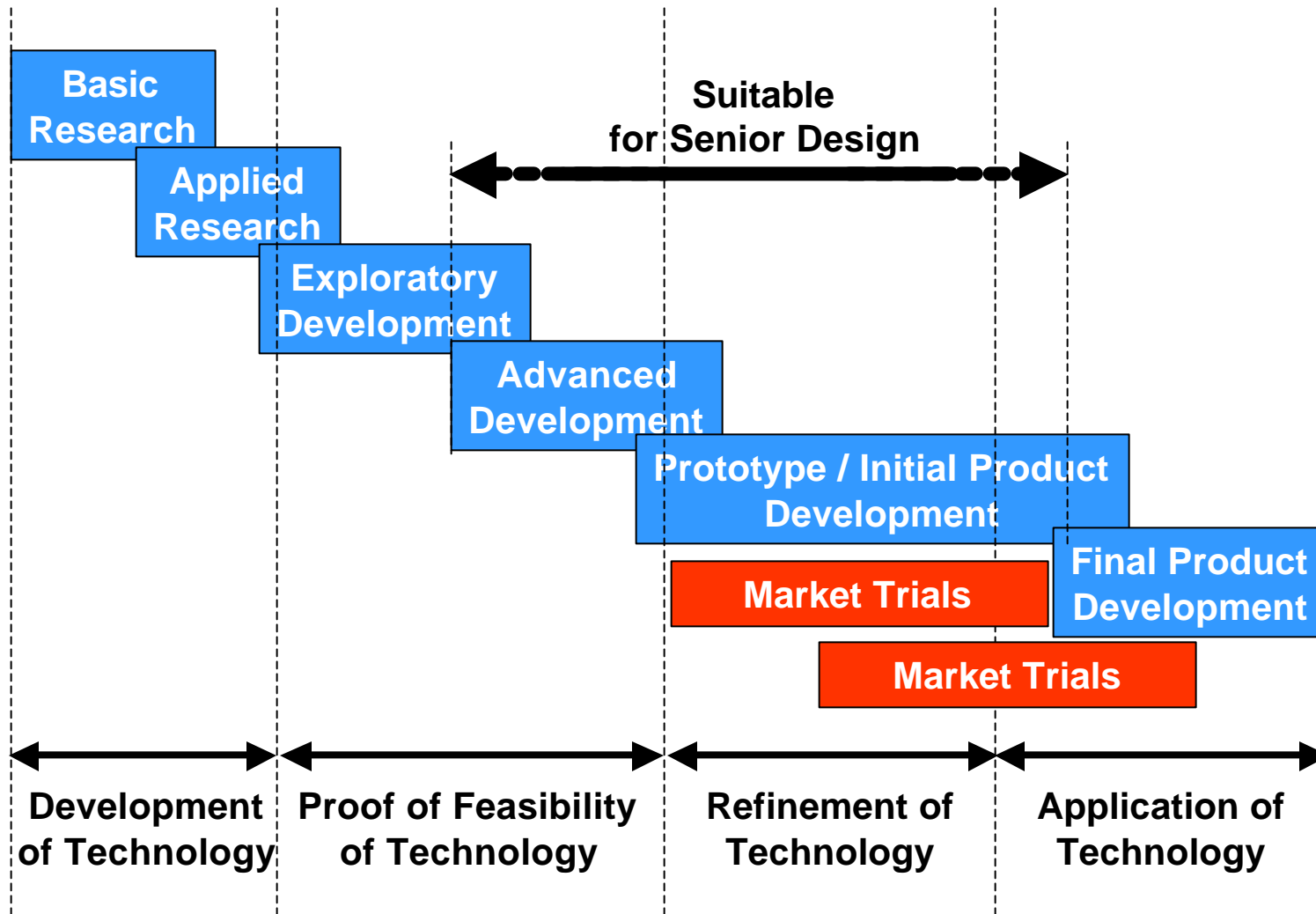
Announcement

- ECE Student Council meeting
 - 9:30 pm, Wednesday, September 10
 - Mott Room (3rd floor, Howe Center)

Class Conflicts



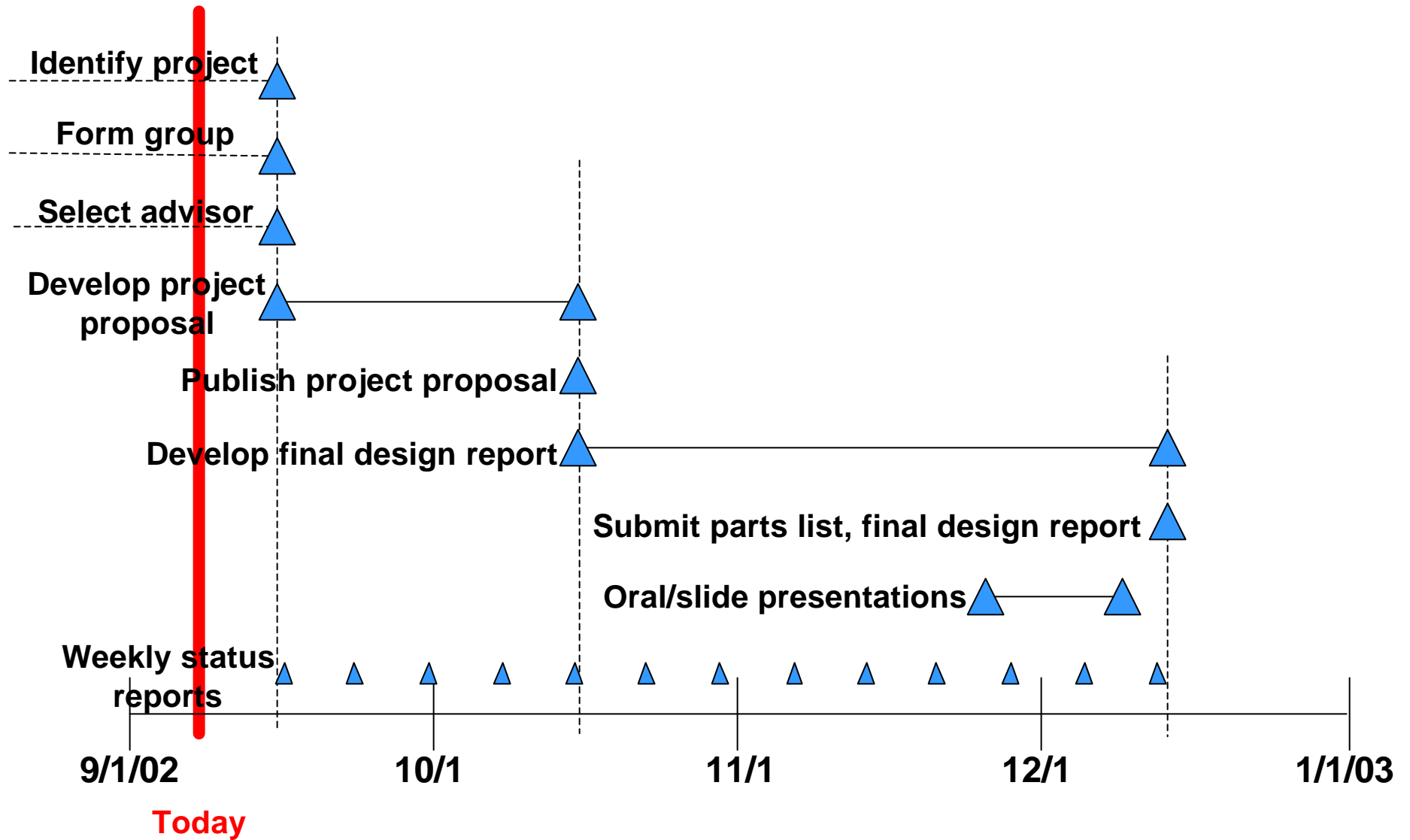
Stages in Development Cycle



Constraints That Can (Should?) Be Relaxed For Prototype

- **Physical size**
 - Level of integration (VLSI vs. FPGA, mC, LSI/MSI, etc.)
 - **Implementation platform**
 - simulating a Palm on a laptop,
 - simulating functions in software that would normally be in hardware, and vice versa
 - **Feature set**
 - What is essential to demonstrate concept, vs. what could be imagined/assumed
 - What is known to be doable vs. what is to be demonstrated
 - **Performance**
 - Speed, capacity, etc.
 - **Environmental constraints**
 - Operating temperature range, shock, vibration, etc.
- Focus on *key* attributes of end design, not every detail

Senior Design Tasks – Fall '03



Upcoming Discussion/Seminar Topics

- Patents, Trade-secrets, Intellectual Property and Technical Business Ethics
- GPS and Geolocation Services
- RS-232 Interfacing with Uncooperative, Non-Standard Devices
- I'll add other topics as they occur to me or students propose them